PATENT

SZS&Z Ref. No.: IO031004PUS Attorney Docket No.: INFN/SZ0018

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NOTHE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Jung Pill Kim et al.

Serial No.: 10/681,647

Confirmation No.: 6096

Filed:

October 8, 2003

For: VOLTAGE TRIMMING CIRCUIT

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Group Art Unit: 2816

Examiner: Hiep Nguyen

Customer No.: 046798

Mail Stop AMENDMENT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATE OF MAILING 37 CFR 1.8

I hereby certify that this correspondence is being deposited on March 16, 2005, with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Viv. 22313, 1450.

March 16, 2005 Date

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INFORMATION DISCLOSURE STATEMENT

The Applicant(s) and the Attorney who signs below, on the basis of the information supplied by the inventor and the information in his file, submit herewith patents, publications, or other information of which they are aware, which may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

While the information submitted in this Information Disclosure Statement may be material pursuant to 37 CFR § 1.56, it is not intended to constitute an admission that any patent, publication, or other information referred to therein is prior art for this invention unless specifically designated as such.

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In accordance with 37 CFR § 1.97, this Information Disclosure Statement is not to be construed as a representation that a search has been made or that no other possibly material information as defined under 37 CFR § 1.56(a) exists.

The patents and/or publications submitted herewith are set forth on the attached Form PTO-1449.

Applicants certify that all references submitted with this disclosure were first cited in a communication, which is enclosed, from a foreign patent office dated January 4, 2005, which is not more than three months prior to the filing of this Disclosure Statement.

Applicants believe that no fee is due for this submission. However, if the sum of \$180.00 is due under 37 CFR § 1.17(p) pursuant to § 1.97, the Commissioner is hereby authorized to charge this fee, and any other fee necessary to make this submission timely, to the Deposit Account No. 20-0782/INFN/SZ0018/GGM.

Respectfully submitted,

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PTO/SB/08a (08-03)

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 2

Application Number	10/681,647	
Filing Date	October 8, 2003	
First Named Inventor	Jung Pill Kim	
Group Art Unit	2816	
Examiner Name	Hiep Nguyen	
Attorney Docket Number	INFN/SZ0018	
Submission Date	March 16, 2005	

U.S. PATENT DOCUMENTS					
Examiner Cite No.1			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	No.	Number-Kind Code ^{2 (If known)}			
	A1	US-2001/0017567 A1	08-30-2001	Kawakubo	
	A2	US-2001/0054760 A1	12-27-2001	Ito et al.	
	A3	US-6,496,027 B1	12-17-2002	Sher et al.	
	A4	US-6,455,952 B1	09-24-2002	Wang	
	A5	US-6,255,895 B1	07-03-2001	Kim et al.	
	A6	US-5,838,076	11-17-1998	Zarrabian et al.	
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	A9	US-			
	A10	US-			
	A11	US-			
	A12	US-			
	A13	US-			
	A14	US-			
	A15	US-			

FOREIGN PATENT DOCUMENTS						
Examiner	Cite	Foreign Patent Document	Publication Date	Name of Patentee or	Pages, Columns, Lines,	T ⁶
Initials*	No.¹		MM-DD-YYYY	Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	
	B1	JP 3062734	03-18-1991	NEC CORP		
	B2	JP 2002 083928 A	03-22-2002	FUJI ELECTRIC CO LTD		
	В3	EP 0 701 259 A	03-13-1996	SAMSUNG ELECTRONICS CO., LTD.		
	B4					
	B5					

Examiner Date Considered

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of 2 PTO/SB/08b (08-03)

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TRADEMARY 10/681,647 Application Number Substitute for form 1449B/PTO Filing Date October 8, 2003 INFORMATION DISCLOSURE First Named Inventor Jung Pill Kim STATEMENT BY APPLICANT **Group Art Unit** 2816 **Examiner Name** Hiep Nguyen (Use as many sheets as necessary) Attorney Docket Number **INFN/SZ0018**

Submission Date

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T²
	C1	PCT International Search Report dated January 4, 2005.	
-	C2	PCT Written Opinion dated January 4, 2005.	
	C3	R. TORRES-TORRES, R.S. MURPHY-ARTEAGA and S. DECOUTERE, <i>MOSFET Gate Resistance Determination</i> , Electronic Letters, 23 rd January 2003, Vol. 39, No. 2, pp. 248-250, IEE STEVENAGE, GB.	
	C4	A. ORTIZ-CONDE et al., Series Resistance and Effective Channel Length Extraction of n-channel MOSFET at 77 K, Electronic Letters, 14 th April 1994, Vol. 30, No. 8, pp. 670-672, IEE STEVENAGE, GB.	
	C5		
	C6		
	C7		
	C8		
	C9		<u> </u>
	C10		

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